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CLAIMS

- 1. A method for modeling analog signals comprising the steps of:
 - (A) detecting one or more attributed analog signals; and
- (B) modeling said attributed analog signals by adding a signature to each of said one or more attributed signals.
- 2. The method according to claim 1, wherein step (B) further comprises:

performing digital simulations with said one or more attributed analog signals.

- 3. The method according to claim 1, wherein each of said one or more signatures corresponds to a predetermined parameter.
- 4. The method according to claim 1, wherein each of said one or more signatures comprises an unique digital signature.
- 5. The method according to claim 1, wherein step (B) further comprises:

0325.00368 CD00059

performing verification of said one or more attributed analog signals.

6. The method according to claim 1, wherein step (B) further comprises:

determining a connectivity of said one or more attributed analog signals.

7. The method according to claim 1, wherein step (B) further comprises:

verifying a model of an analog block configured to receive at least one of said one or more attributed analog signals.

8. The method according to claim 7, wherein step (B) further comprises:

verifying a second one or more models of analog blocks configured to receive another at least one of said one or more attributed analog signals.

9. A method for testing a model of a device, comprising the steps of:

0325.00368 CD00059

- (A) performing tests on said model; and
- (B) verifying connectivity of one or more attributed analog signals within said model by verifying one or more signatures associated with each of said one or more attributed analog signals.
- 10. The method according to claim 9, wherein step (B) further comprises:

if said one or more attributed analog signals are not verified, disabling said device.

- 11. The method according to claim 9, wherein step (B) further comprises verifying a model of an analog block configured to receive at least one of said one or more attributed analog signals.
- 12. The method according to claim 11, wherein step (B) further comprises verifying a second one or more models of analog blocks configured to receive another at least one of said one or more attributed analog signals.

0325.00368 CD00059

- 13. A method for verifying a model of a device comprising the steps of:
 - (A) detecting one or more attributed analog signals; and
- (B) performing one or more verifications of one or more predetermined parameters in response to said one or more attributed analog signals.
- 14. The method according to claim 13, wherein step (A) further comprises:

adding a signature to at least one of said one or more attributed analog signals.

- 15. The method according to claim 14, wherein said signature comprises an unique digital signature.
- 16. The method according to claim 13, wherein said one or more predetermined parameters comprise one or more digital parameters.

0325.00368 CD00059

- 17. The method according to claim 13, wherein said one or more predetermined parameters comprise one or more analog parameters.
- 18. The method according to claim 13, wherein step (B) further comprises determining a connectivity of said one or more attributed analog signals.
- 19. The method according to claim 13, wherein step (B) further comprises verifying a model of an analog block in response to said one or more attributed analog signals.
- 20. The method according to claim 13, wherein step (A) further comprises:

adding a first signature to at least one of said one or more attributed analog signals; and

adding a second signature to said at least one attributed analog signal, wherein said first signature corresponds to a first predetermined parameter of said one or more predetermined parameters and said second signature corresponds to a second

0325.00368 CD00059

predetermined parameter of said one or more predetermined parameters.